Electronic Acknowledgement Receipt

EFS ID:	1023438
Application Number:	10682269
Confirmation Number:	8272
Title of Invention:	Asymmetrical reset transistor with double-diffused source for CMOS image sensor
First Named Inventor:	Dun-Nian Yaung
Correspondence Address:	George O. Saile - 28 Davis Avenue - Poughkeepsie NY 12603 US - -
Filer:	Daniel R. McClure
Filer Authorized By:	
Attorney Docket Number:	TS02-026B
Receipt Date:	14-APR-2006
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Application Type:	Utility
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Submitted with Payment	no

File Listing:

Document Number	Document Description	File Name	File Size(Bytes)	Multi Part	Pages
1	Amendment - After Non-Final Rejection	00366675.pdf	311105	no	7
Warnings:					
Information:					
2	Power of Attorney (may include Associate POA)	00366676.pdf	179463	no	4
Warnings:					
Information:					
3	Assignee showing of ownership per 37 CFR 3.73(b).	00366677.pdf	77655	no	2
Warnings:					
Information:					
		Total Files Size (in bytes):	568223		

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New Applications Under 35 U.S.C. 111

If a new application is being filed and the application includes the necessary components for a filing date (see 37 CFR 1.53(b)-(d) and MPEP 506), a Filing Receipt (37 CFR 1.54) will be issued in due course and the date shown on this Acknowledgement Receipt will establish the filing date of the application.

National Stage of an International Application under 35 U.S.C. 371

If a timely submission to enter the national stage of an international application is compliant with the conditions of 35 U.S.C. 371 and other applicable requirements a Form PCT/DO/EO/903 indicating acceptance of the application as a national stage submission under 35 U.S.C. 371 will be issued in addition to the Filing Receipt, in due course.